



NON-PROVISIONAL APPLICATION FOR U. S. PATENT UNDER 37 CFR 1.53(b)

TRANSMITTAL FORM

Attorney Docket No. **TI-34815**

BOX PATENT APPLICATION
Commissioner of Patents
P. O. Box 1450
Alexandria, VA 22313-1450

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Date of Deposit **August 27**, 2003:
I hereby certify that this paper is being deposited with the U.S. Postal Service
Express Mail Post Office to Addressee Service under 37 CFR 1.10 on the date
shown above and is addressed to the **BOX PATENT APPLICATION**,
Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.

Lon Doyle
Lon Doyle

Sir:

Transmitted herewith for filing is the
patent application of:

Inventor(s): **Xiao Hong Du, et al**

For: **CMOS VOLTAGE BOOSTER CIRCUIT**

Enclosed are:

- 9 Sheets of informal drawings and 14 pages of Specification (including Abstract)
- ☒ A Declaration/Power of Attorney
- ☒ Assignment along with PTO 1595

☒ This application is a continuation-in-part of co-pending commonly assigned U.S. Patent Application
Serial Number 10/337,053 entitled "CMOS Voltage Booster Circuits" filed January 6, 2003 of which
priority is hereby claimed

FEE CALCULATION					FEE
	NUMBER		NUMBER EXTRA	RATE	BASIC FEE \$ 750.00
Total Claims	21	-20 =	1	X \$18 =	18.00
Independent Claims	1	-3 =	0	X \$84 =	0.00
Total Filing Fee					\$768.00

Please charge Deposit Account No. 20-0668 in the amount of the total fees set forth. The Assistant Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 20-0668.

All correspondence related to this application may be addressed to Dan Swayze at Texas Instruments Incorporated, P. O. Box 655474, M/S 3999, Dallas, Texas 75265 and (972) 917-5633.

August 27, 2003
Dated

Robert C. Klinger
Robert C. Klinger
Reg. No. 34,365

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